Parson 3-2-1-4



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450,

Applicant(s): D.E. Parson et al.

Case:

3-2-1-4

Serial No.:

09/583,057

Filing Date:

May 30, 2000

Group:

2114

Examiner:

Timothy M. Bonura

Title:

Control Method and Apparatus for Testing of Multiple

Processor Integrated Circuits and Other Digital Systems

TRANSMITTAL OF SUPPLEMENTAL APPEAL BRIEF

Mail Stop Appeal Brief - Patents Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

- (1) Response to Office Action; and
- (2) Supplemental Appeal Brief in triplicate (original and two copies).

Please extend the period for response by one month to September 10, 2004. Please charge Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762 the amount of \$110, to cover this fee. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Deposit Account No. 50-0762 as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

Date: September 10, 2004

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